



#16/10
J. Brown
PATENT
9/12/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

MANFRED HORSTMANN
ROLF STEPHAN
KARSTEN WIECZOREK
STEPHEN KRUEGEL

Serial No.: 09/847,622

Filed: May 2, 2001

For: FIELD EFFECT TRANSISTOR WITH
REDUCED GATE DELAY AND METHOD
OF FABRICATING THE SAME

Examiner: Jesse A. Fenty

Group Art Unit: 2815

Att'y Docket: 2000.064200/DE0053

Customer No.: 23720

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TECHNOLOGY CENTER 2800

RESPONSE TO OFFICE ACTION DATED JUNE 30, 2003

CERTIFICATE OF MAILING
37 C.F.R 1.8

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date below:

August 29, 2003

Date


Signature

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This paper is submitted in response to the Office Action dated June 30, 2003, for which the three-month date for response is September 30, 2003.

A fee in the amount of \$228.00 is believed to be due in connection with the present paper. **The Director is authorized to deduct such fee, and any other fees required under 37 C.F.R. §§ 1.16 to 1.21, from Advanced Micro Devices, Inc. Deposit Account No. 01-0365/DE0053.** In the event the monies in that account are insufficient, the Director is authorized to withdraw funds from Williams, Morgan & Amerson, P.C. Deposit Account No. 50-0786/2000.064200.

Reconsideration of the application in view of the following amendments and remarks is respectfully requested.

09/04/2003 HDEMESS1 00000064 010365 09847622

01 FC:1201 84.00 DA
02 FC:1202 144.00 DA